

Overview



More Synchronization

Monitors Simpler Mechanisms



The Story So Far...



Mutual Exclusion

Basic idea?

Semaphores

Basic idea?



Just how fair do we need to be...?

Our Take...



Just how fair do we need to be...?

Our Take...

Fairness

No one likes semaphores!

- Too low-level
- Too much freedom (& too strange)
- Too hard to get right

Need an alternative...

Monitors were devised as an alternative to semaphores

More Synchronization

- High-level synchronization construct, based on classes
- Only one task can be running "inside" the class at a time Declare classes like this:

```
monitor class MyClass {
    public:
        /* method declarations only
    private:
        /* private data and private methods */
};
```





Exit

Monitors

More Synchronization Monitor

Basic idea

- Only one process can be in the monitor at a time
- cwait (beer) waits for beer
- csignal(beer) signals beer





Monitors

More Synchronization

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More Synchronization Monitors Equivalence Claims



Equivalence Claim

How could we show that Semaphores aren't "more powerful" than monitors? Monitors aren't "more powerful" than semaphores?

How could we show that

- Semaphores aren't "more powerful" than monitors?
- Monitors aren't "more powerful" than semaphores?



In NP-completeness, you learn SAT, and then the simpler 3-SAT, which is equivalent.

Can we imagine something "less" than semaphores?

Binary Semaphores

Basic idea?



A binary semaphore is similar to test-and-set. If it's nonzero, one one process can set it to zero and continue past <code>bsem_dec</code>. If it's zero, <code>bsem_inc</code> sets it nonzero and wakes at least one process waiting on it. Multiple calls to <code>bsem_inc</code> with no intervening <code>bsem_dec</code> will have no effect. However, it is illegal to do that: you can't call <code>bsem_inc</code> unless the semaphore value is currently zero.

Assume the following binary semaphore operations:

```
struct bsem* bsem_create (int count);
void bsem_dec (struct bsem* s);
void bsem_inc (struct bsem* s);
```

Data to implement semaphores...?



Assume the following binary semaphore operations:

```
struct bsem* bsem_create (int count);
void bsem_dec (struct bsem* s);
void bsem_inc (struct bsem* s);
```

Data to implement semaphores...?

struct sem {
 volatile int count; // Semaphore count

struct bsem* wait; // Wait here...



aphores from Binary Semaphores

Assume the following binary semaphore operations

struct bsem. bsem_create (int count) void bsem_dec (struct bsem. s); void bsem_inc (struct bsem. s);

Data to implement semaphores...?

struct sem { volatile int count; // Semaphore count struct beem+ wait; // Nait bere...

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struct bsem* bsem_create (int count);
void bsem_dec (struct bsem* s);
void bsem_inc (struct bsem* s);
```

Data to implement semaphores...?

struct sem {
 volatile int count; // Semaphore count
 struct bsem* wait; // Wait here...

struct bsem* mutex; // Protects count



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Data to implement semaphores...?

struct sem {
 volatile int count; // Semaphore count
 struct bsem* wait; // Wait here...
 struct bsem* mutex; // Protects count
 volatile int waiting; // How many waiting



aphores from Binary Semaphore

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struct bsem* bsem_create (int count);
void bsem_dec (struct bsem* s);
void bsem_inc (struct bsem* s);
```

Data to implement semaphores...?

```
struct sem {
  volatile int count; // Semaphore count
    // +val = sem count, -val = wait count
  struct bsem* wait; // Wait here...
  struct bsem* mutex; // Protects count
```



aphores from Binary Semaphores

Assume the following binary semaphore operations:

struct bsem. bsem_create (int count) void bsem_dec (struct bsem. s); void bsem_inc (struct bsem. s);

Data to implement semaphores...?

truct sea {
 volatile int count; // Semaphore count
 // +val = sea count, -val = wait count
 struct been= mutey; // Nait bere...
 struct been= mutey; // Protects count

Assume the following binary semaphore operations:

```
struct bsem* bsem_create (int count);
void bsem_dec (struct bsem* s);
void bsem_inc (struct bsem* s);
```

Data to implement semaphores...?

};

```
struct sem {
  volatile int count; // Semaphore count
    // +val = sem count, -val = wait count
    struct bsem* wait; // Wait here...
    struct bsem* mutex; // Protects count
```



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 volatile int count; // Semaphore count
 // +val = sem count, -val = wait count
 struct been= mutex; // Wait bere...
 struct been= mutex; // Protects count

Semaphores from Binary Semaphores (cont.)



Initialization:

```
struct sem* sem init(int count)
  struct sem* s = malloc(sizeof(struct sem));
  assert(s != NULL && count >= 0);
  s \rightarrow count = count;
  s->mutex = bsem_create(1); // Ordinary mutex
  s->wait = bsem_create(0); // Mostly locked,
                              // briefly unlocked
  return s;
```

Semaphores from Binary Semaphores (cont.)

Is this code okay?

```
void sem dec(struct sem* s) void sem inc(struct sem* s)
  bsem_dec(s->mutex);
                                  bsem_dec(s->mutex);
  --(s->count);
                                  ++(s->count);
 if (s \rightarrow count < 0) {
                                  if (s \rightarrow count <= 0) {
                                    bsem_inc(s->wait);
    bsem_inc(s->mutex);
    bsem dec(s->wait);
  else
    bsem inc(s->mutex);
                                  bsem inc(s->mutex);
```

CS34 σ More Synchronization -2013-05 Simpler Mechanisms -Semaphores from Binary Semaphores (cont.)

There is a race after sem_dec calls bsem_inc on the mutex; we could sleep on s->wait even though s->count has become nonzero. We need to ensure that there is exactly one bsem_inc per wait. For example:

- 1. Process 1 decs and stops after mutex release
- Process 2 decs and stops after mutex release
- Process 3 incs and bumps wait
- 4. Process 4 incs and re-bumps wait (illegally)
- 5. Process 1 continues and passes through wait
- 6. Process 2 continues and waits forever

Semaphores from Binary Semaphores (cont.)

Does this version fix the problem?

```
void sem dec(struct sem* s) void sem inc(struct sem* s)
  bsem_dec(s->mutex);
                                bsem_dec(s->mutex);
  --(s->count):
                                ++(s->count);
  if (s - count < 0) {
                                if (s \rightarrow count <= 0) {
    bsem_inc(s->mutex);
                                   bsem inc(s->wait);
    bsem dec(s->wait);
                                else
  bsem inc(s->mutex);
                                    bsem inc(s->mutex);
```

CS34 σ More Synchronization inas this version fix the nonhia -2013-05 Simpler Mechanisms -Semaphores from Binary Semaphores (cont.)

The assumption here is that if sem_dec waits, sem_inc would grab the mutex on its behalf and bump wait. So even if somebody else gets in between the release of the mutex and the wait, they will necessarily allow us to pass through. In our previous scenario:

- 1. Process 1 decs and stops after mutex release
- Process 2 decs and stops after mutex release
- Process 3 incs and bumps wait
- 4. Because the mutex is still held, process 4 can't proceed. Instead, one of process 1 & 2 will continue and pass through the wait.
- Process 1 continues and releases mutex.
- Process 4 incs and bumps wait
- 7. Process 2 can now continue and pass through wait.

Monitors Revisited





Basicidea • Only one process can be in the monitor at a time • cvarit (beer) walks for beer • casignal (beer) signals bee	

Basic idea

- Only one process can be in the monitor at a time
- cwait (beer) waits for beer
- csignal(beer) signals beer



Monitors without Condition Variables

Basic idea

 Only one process can be in the monitor at a time

Remind you of anything?





CS34 More Synchronization Simpler Mechanisms Mutexes / Locks

Like binary semaphores, but with ownership rules: • You "acquie" the lock • You "hold" the lock • You "hold" the lock Someone else can't release it for you.

Like binary semaphores, but with ownership rules:

- ► You "acquire" the lock
- You "hold" the lock
- ► You "release" the lock

Someone else can't release it for you.

More Synchronization Simpler Mechanisms		
Mutexes / Locks void task(const int i)	CS34 More Synchronization Simpler Mechanisms Mutexes / Locks	<pre>Macess / Lods viid taat(const int i)</pre>
<pre>{ for (;;) { lock_acquire(ourlock); critical_section_actions(i); lock_release(ourlock); other_actions(i); }</pre>		

Class Exercise

Is bool lock_tryacquire(lock) useful?

Class Exercise

Can you implement semaphores using mutexes?

Can you implement mutexes using semaphores?

What do mutexes remind you of?



Class Exercise

Can you implement semaphores using mutexes? Can you implement mutexes using semaphores?

What do mutexes remind you of?



More Synchronization Simpler Mecha

Condition Variables (for Mutexes)

What are the operations?

What are the arguments?



21/21



More Synchronization Simpler Mecha

Condition Variables (for Mutexes)

What are the operations?

What are the arguments?

Do you need to hold the lock when you cond_signal or cond_broadcast?



