

Intermediate Representations

CS 132: Compiler Design

Monday, February 23, 2011

TYPES OF INTERMEDIATE REPRESENTATIONS

- ✓ Graphical
 - ▶ Trees
 - ▶ Graphs
- ✓ Linear
 - ▶ E.g., *real or idealized assembly code*
- ✓ In-Between (*Hybrid*)
 - ▶ E.g., *graphs of linear basic blocks*
 - ▶ *Combination of linear code + graphical information*

DEFINITION: BASIC BLOCK

A maximal sequence of instructions that is only entered at the first instruction and that may leave the sequence at the last instruction

TYPES OF INTERMEDIATE REPRESENTATIONS

- ✓ High-Level
 - ▶ Abstract primitives (invoking a virtual method, creating tuples, ...)
 - ▶ Structured control

- ✓ Low-Level
 - ▶ Assembly-level and/or hardware specific operations
 - ▶ Data representations exposed

- ✓ In-Between
 - ▶ Mostly low-level operations, plus a few “powerful” primitives
 - ▶ E.g., invoke a method, throw/catch and exception

ADVANTAGES OF HIGH-LEVEL/LOW-LEVEL REPRESENTATIONS

```
val a = (1,3)
val b = (1,3)
```

```
a = malloc(8)
*a = 1
*(a+4) = 3
b = malloc(8)
*b = 1
*(b+4) = 3
```

```
val a = (1,3)
val b = (5,7)
```

```
a = malloc(8)
*a = 1
*(a+4) = 3
b = malloc(8)
*b = 5
*(b+4) = 7
```

LINEAR REPRESENTATIONS

- ✓ *Stack code* (JVM, CIL, ...)
- ✓ *Three-address code* (a.k.a. RTL, Quadruples)
 - ▶ *Pseudo-assembly*: very simple operations
 - ▶ *Arbitrarily many temporaries*
 - ▶ *Conditional jumps to labels*
 - ▶ *Primitives for call/return*
 - ▶ *Memory load, stores*
- ✓ *Assembly language* (x86, PowerPC, ...)

$i \leftarrow 1$

$j \leftarrow 1$

$k \leftarrow 0$

L1:

if $k \geq 100$ goto done

if $j \geq 20$ goto L2

$j \leftarrow i$

$k \leftarrow k + 1$

goto L3

L2:

$j \leftarrow k$

$k \leftarrow k + 2$

L3:

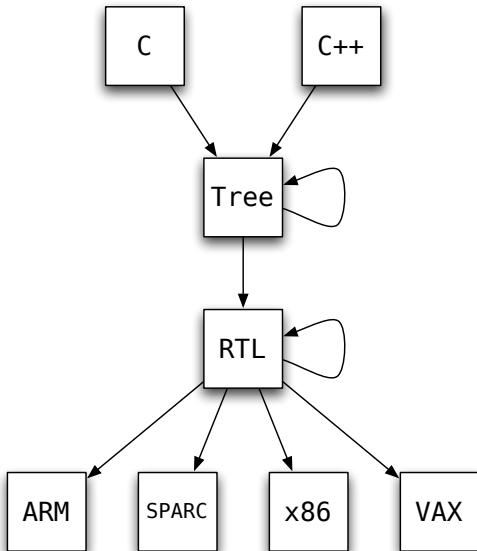
$j \leftarrow j - 1$

goto L1

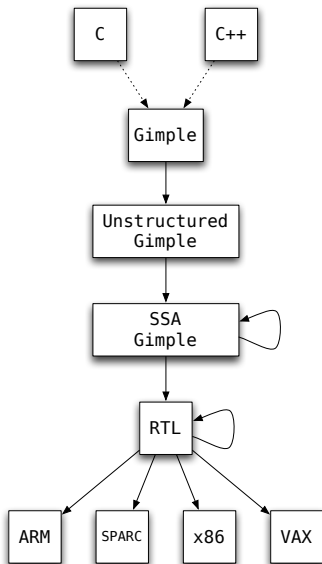
GRAPHICAL REPRESENTATIONS

- ✓ Parse trees
- ✓ Abstract syntax trees
 - ▶ Source language
 - ▶ Intermediate languages
 - ▶ Assembly language
- ✓ Directed Acyclic Graphs
 - ▶ Express sharing among subexpressions
- ✓ Graphs
 - ▶ Control flow: nodes are expressions or basic blocks, arrows if control can pass from one node to the other.
 - ▶ Dependence: nodes are expressions, arrows if a value created by the source will be used by the target.
 - ▶ ...

GCC 3.x



GCC 4.x



GENERIC and GIMPLE

GENERIC

```
if (foo (a + b,c))
  c = b++ / a
endif
return c
```

High GIMPLE

```
t1 = a + b
t2 = foo (t1, c)
if (t2 != 0)
  t3 = b
  b = b + 1
  c = t3 / a
endif
return c
```

Low GIMPLE

```
t1 = a + b
t2 = foo (t1, c)
if (t2 != 0) <L1, L2>
L1:
  t3 = b
  b = b + 1
  c = t3 / a
  goto L3
L2:
L3:
  return c
```

STATIC SINGLE ASSIGNMENT

Idea: every variable is written to exactly once in the code

$j \leftarrow 0$

$k \leftarrow 2$

$k \leftarrow k + 1$

$j \leftarrow j + k$

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$j \leftarrow 0$

$k \leftarrow 2$

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$j \leftarrow j + k$

$j1 \leftarrow 0$

$k1 \leftarrow 2$

$k2 \leftarrow k1 + 1$

$j2 \leftarrow j1 + k2$

Advantage: Some optimizations are easier/more efficient

Difficulties?

DRAW THE CONTROL-FLOW GRAPH; CONVERT TO SSA

```
i ← 1; j ← 1; k ← 1;
while (k < 100) do {
  j ← j - 1
  if (j < 20) then {
    j ← i
    k ← k+1
  } else {
    j ← k
    k ← k+2
  }
}
```

return;

PHI FUNCTIONS TO THE RESCUE!

Assume a control-flow node has n incoming arrows.

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Construct an SSA Control-Flow graph for the last code sequence.

TRIVIALY EASY SSA OPTIMIZATIONS

1. We can eliminate the definition $x_i \leftarrow c$ (where c is a constant) by ...
2. We can eliminate the definition $x_i \leftarrow y_j$ (where y_j is a variable) by ...
3. An assignment $x_i \leftarrow e$ can be eliminated if ...

IMPLEMENTING SSA

Remaining issues

1. How can we actually implement code written with ϕ functions?

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Remaining issues

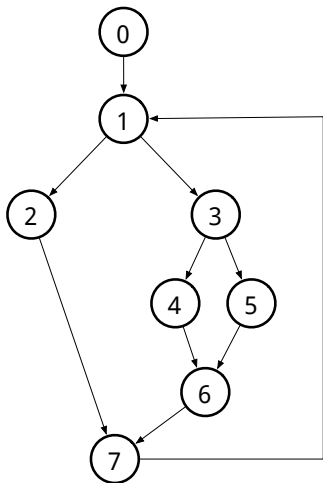
1. How can we actually implement code written with ϕ functions?
2. How can we place the ϕ functions efficiently?

DOMINATORS

In a CFG, B_1 **dominates** B_2 if every path from the start of the code to B_2 must pass through B_1 .

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Why?

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Why?

Efficiency improvement: work with **immediate dominators**.

USING DOMINANCE FRONTIERS

The **dominance frontier** $DF(n)$ of a node n is the set of nodes m such that

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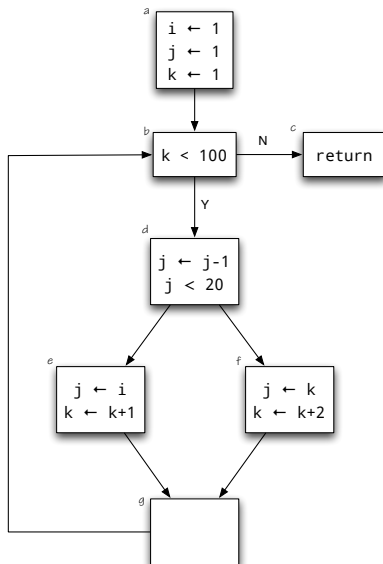
Intuitively, look at all the paths going out of n ; as soon as you find a node no longer strictly dominated by n , you're in the frontier.

A definition of x in node b forces a ϕ -function for x in every element of $DF(b)$.

$$x = \phi(x, \dots, x)$$

These new ϕ functions act as new definitions, so we have to iterate this process. When done, go through and rename.

EXAMPLE



OPTIONAL PRUNING

The dominance-frontier method produces “minimal SSA”

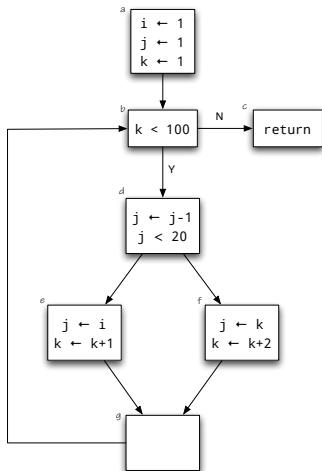
- ✓ ϕ -functions exactly where

But we could prune further:

- ✓ If a variable is *dead* in a block, don't insert a ϕ function there.
- ✓ Even more simply (but less optimally), only insert ϕ functions that are *live* at the end of *some* basic block.

A SIMPLER ALGORITHM: “MAXIMAL SSA”

- ✓ Put a ϕ function for every variable at the start of every block!
- ✓ With care, we can add ϕ functions and do all renaming in a single pass.



OPTIMIZING MAXIMAL SSA

Some ϕ functions can be easily removed

- ✓ $v_i \leftarrow \phi(v_i, \dots, v_i)$ (trivially)
- ✓ $v_i \leftarrow \phi(v_i, v_j, v_i, v_j, v_j)$ (via substitution)

Theorem: Repeatedly remove such ϕ functions. If the graph is *reducible*, the final result will be minimal SSA.

OTHER REPRESENTATIONS: λ -CALCULUS BASED

```
i ← 1; j ← 1; k ← 1;
while (k < 100) do {
  j ← j - 1
  if (j < 20) then {
    j ← i
    k ← k+1
  } else {
    j ← k
    k ← k+2
  }
}
return;
```


OTHER REPRESENTATIONS: CONTINUATION-PASSING STYLE

```
fib n =  
  if (n == 0) then  
    0  
  else if (n == 1) then  
    1  
  else  
    fib (n-1) + fib (n-2)
```

OTHER REPRESENTATIONS: CONTINUATION-PASSING STYLE

```
fib n =  
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    0  
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    1  
  else  
    fib (n-1) + fib (n-2)
```

```
fib n k =  
  if (n == 0) then  
    k 0  
  else if (n == 1) then  
    k 1  
  else  
    fib (n-1) (\x ->  
      fib (n-2) (\ y ->  
        k (k+y)))
```